

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231

Inventor: **Yutaka Doi**  
Serial No: **Div. of 09/752,408**  
Filed: **December 28, 2000**  
For: **Layered Circuit Boards and  
Methods of Production  
Thereof**

Examiner: **Jeffrey Gaffin**  
Art Unit: **2841**

#2  
PreMedmed  
Wicker  
5-24-02

PRELIMINARY AMENDMENT

Box Patent Application  
Assistant Commissioner for Patents  
Washington, D.C. 20231

Dear Mr. Gaffin:

Please enter the following preliminary amendment:

IN THE CLAIMS

Please cancel all pending claims and add the following claims addressing subject matter that was subject to a restriction requirement in application serial number 09/752,408:

142 (Added) A method for producing an electronic component, comprising:  
providing a substrate layer;  
providing a solid, substantially planar optical wave-guide; and  
laminating the solid, substantially planar optical wave-guide onto the substrate layer.

22 21  
15 (Added) The method of claim 14, wherein at least one of a laminating material or a cladding material is coupled to the wave-guide.

23 22  
16 (Added) The method of claim 15, wherein at least one of an additional layer is coupled to the laminating material or the cladding material.

Sub  
B1  
FOOTNOT 32462001  
Rev 1.26